

# Notice of Allowability

Application No.

10/771,465

Examiner

Matthew Bradley

Applicant(s)

KANAI, HIROKI

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## -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment dated 5/1/07.
2. ☒ The allowed claim(s) is/are 6,7,13 and 14.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☒ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☒ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

### Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date \_\_\_\_\_
4. ☐ Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

## **DETAILED ACTION**

### ***Response to Amendment***

This Office Action has been issued in response to amendment filed 1 May 2007.

### ***Claim Status***

Claims 6-7 and 13-14 remain pending and are ready for examination.

### ***Allowable Subject Matter***

Claims **6-7** and **13-14** are allowed.

The following is an Examiner's statement of reasons for allowance: the prior art made of record fails to teach the combination including the limitation of:

(Claim 6) "...wherein the first processor writes into the third memory first data transfer information containing information indicating the storage position of the writing data in the first memory, the second processor writes into the second memory second data transfer information containing information indicating the storage position of the writing data in the second memory and transmits information indicating the storage position of the second data transfer information to the first processor, the first processor transmits the data transfer device transfer start information containing information indicating the storage position of the first data transfer information and information indicating the storage position of the second data transfer information, the data transfer device reads out the second data transfer information from the second memory based on the transfer start information, reads out the first data transfer information from the third memory based on the transfer start information and transfers the writing data from the second memory to the first memory based on the first data transfer information and

the second data transfer information, and the disk controller writes into the storage volume the writing data stored in the first memory on the basis of the write request. ...”;

(Claim 7) “...the first processor writes into the third memory data transfer information containing information indicating the storage position of the read-out data in the first memory, the second processor writes into the second memory second data transfer information containing information indicating the storage position of the read-out data in the second memory and transmits information indicating the storage position of the second data transfer information to the first processor, the first processor transmits to the data transfer device transfer start information containing information indicating the storage position of the first data transfer information and information indicating the storage position of the second data transfer information, the data transfer device reads out the second data transfer information from the second memory based on the transfer start information, reads out the first data transfer information from the third memory based on the transfer start information and transfers the read-out data from the first memory to the second memory on the basis of the first data transfer information and the second data transfer information, and the second processor transmits the read-out data stored in the second memory to the information processing device. ...”;

(Claim 13) “...writing, by the first processor, into the third memory first data transfer information containing information indicating the storage position of the writing data in the first memory; by the second processor, writing into the second memory second data transfer information containing information indicating the storage position of the writing data in the second memory, and transmitting information indicating the

storage position of the second data transfer information to the first processor; transmitting, by the first processor, the data transfer device transfer start information containing information indicating the storage position of the first data transfer information and information indicating the storage position of the second data transfer information; and by the data transfer device, reading out the second data transfer information from the second memory based on the transfer start information, reading out the first data transfer information from the third memory based on the transfer start information, and transferring the writing data from the second memory to the first memory based on the first data transfer information and the second data transfer information; and writing, by the disk controller into the storage volume the writing data stored in the first memory based on the write request. ...”;

(Claim 14) "...writing, by the first processor, into the third memory first data transfer information containing information indicating the storage position of the read-out data in the first memory; by the second processor, writing into the second memory second data transfer information containing information indicating the storage position of the read-out data in the second memory and transmitting information indicating the storage position of the second data transfer information to the first processor; transmitting, by the first processor, to the data transfer device transfer start information containing information indicating the storage position of the first data transfer information and information indicating the storage position of the second data transfer information; by the data transfer device, reading out the second data transfer information from the second memory based on the transfer start information, reading

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out the first data transfer information from the third memory based on the transfer start information and transferring the read-out data from the first memory to the second memory based on the first data transfer information and the second data transfer information; and transmitting, by the second processor the read-out data stored in the second memory to the information processing device. ...”;

The prior art made of record neither anticipates nor renders obvious the above-recited combinations as noted above and as shown in Applicant arguments filed 18 December 2006.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

### ***Response to Arguments***

Applicant's arguments filed 1 May 2007 have been carefully and fully considered, and are persuasive. As noted *supra* the case is in condition for allowance.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matthew Bradley whose telephone number is (571) 272-8575. The examiner can normally be reached on 6:30-3:00 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Donald A. Sparks can be reached on (571) 272-4201. The fax phone

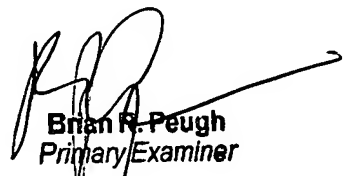
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number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

BRP/mb

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Brian R. Peugh  
Primary Examiner  
